

[AMENDED DRAWINGS]

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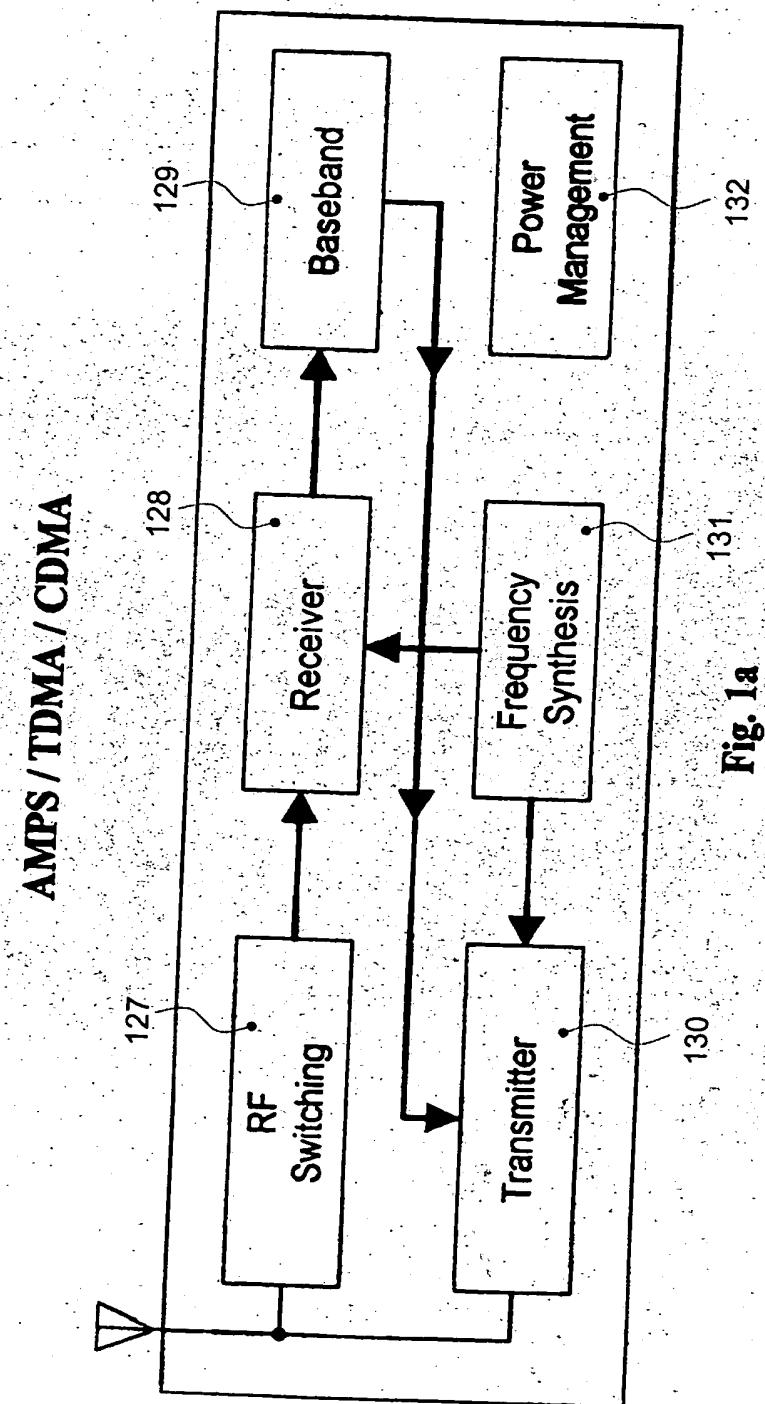
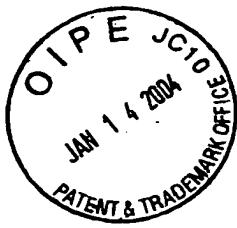


Fig. 1a



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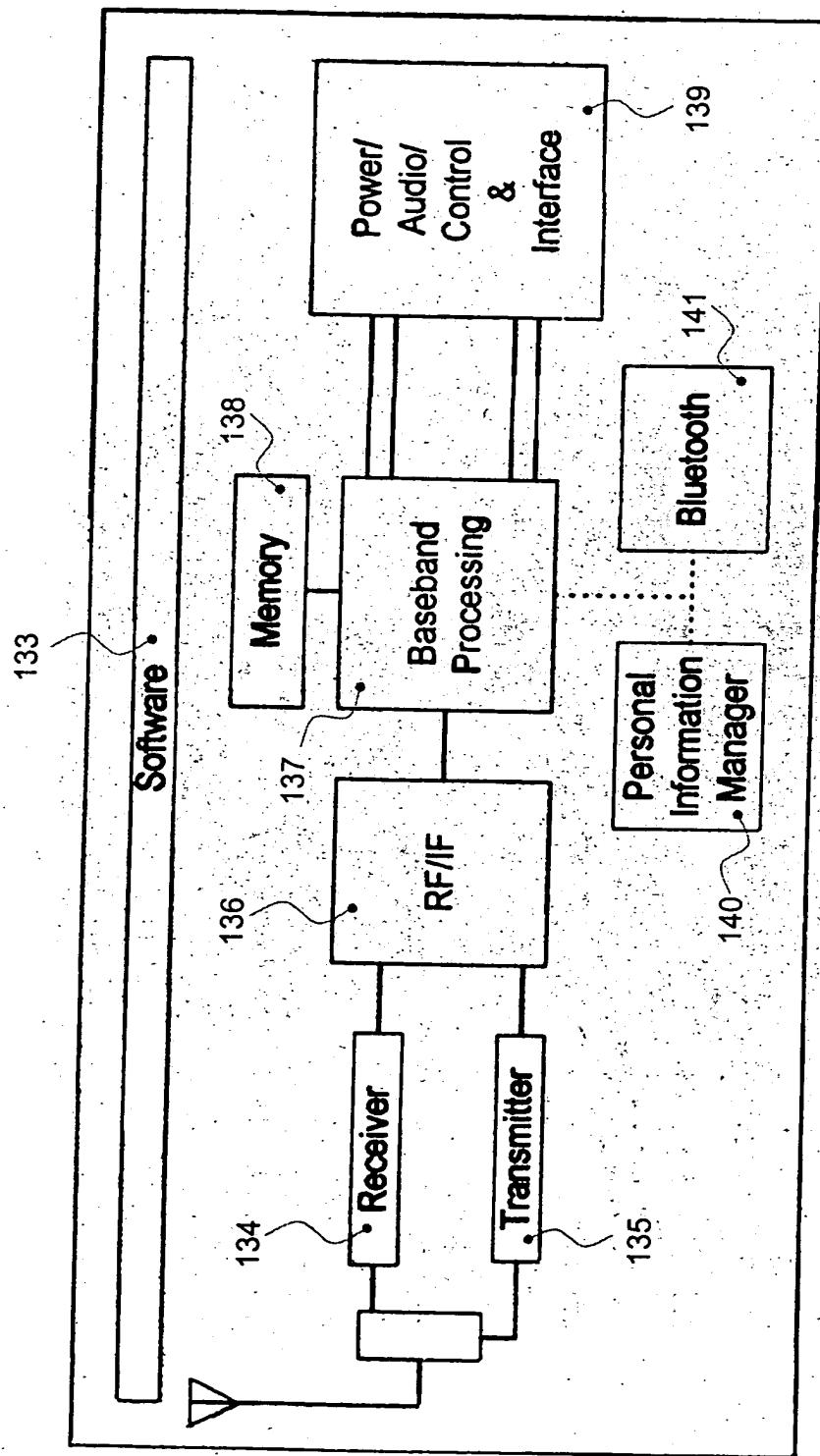
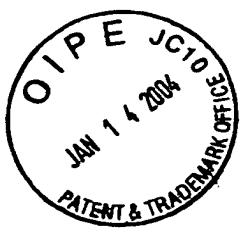


Fig. 1b



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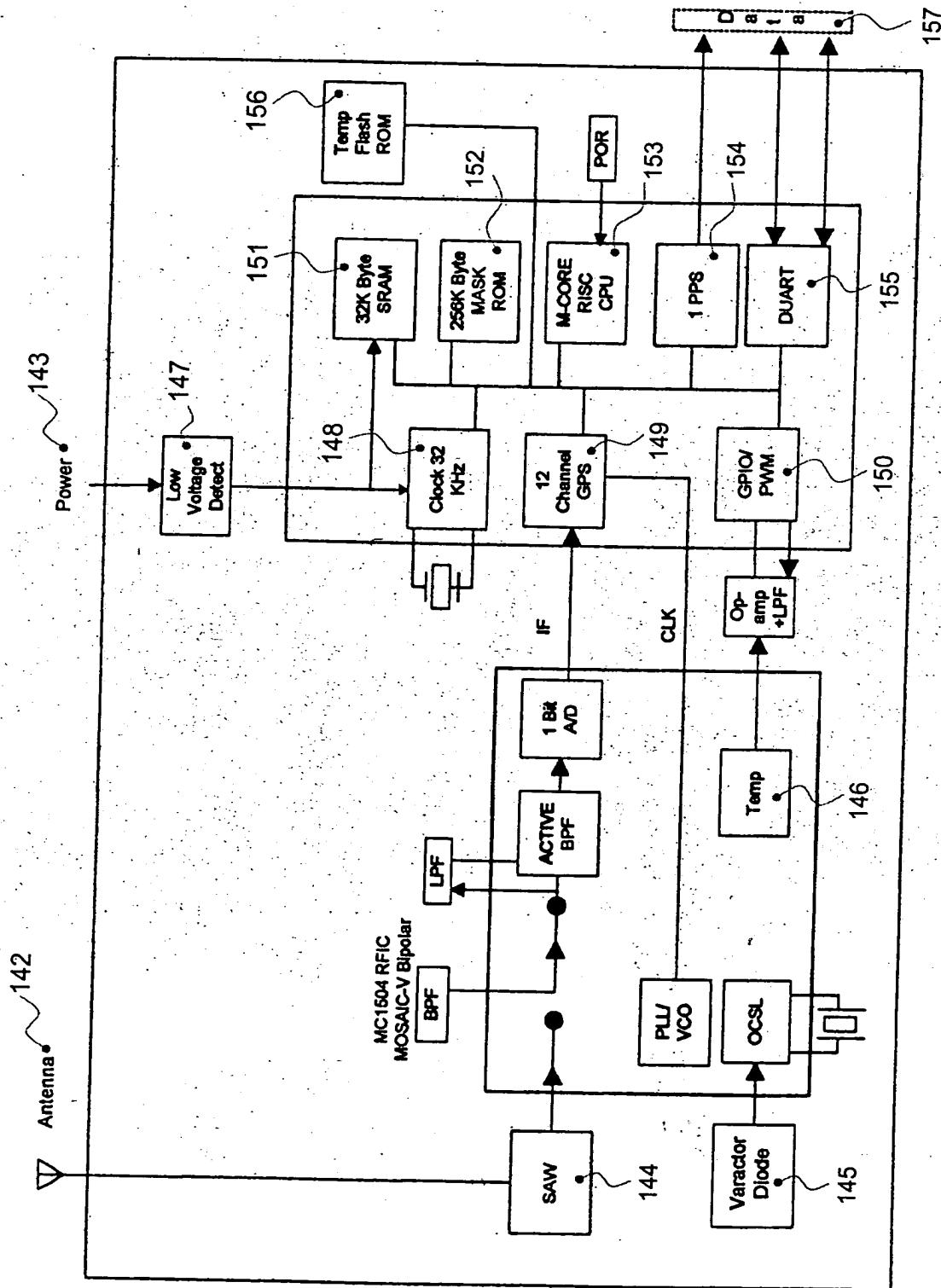


Fig. 2



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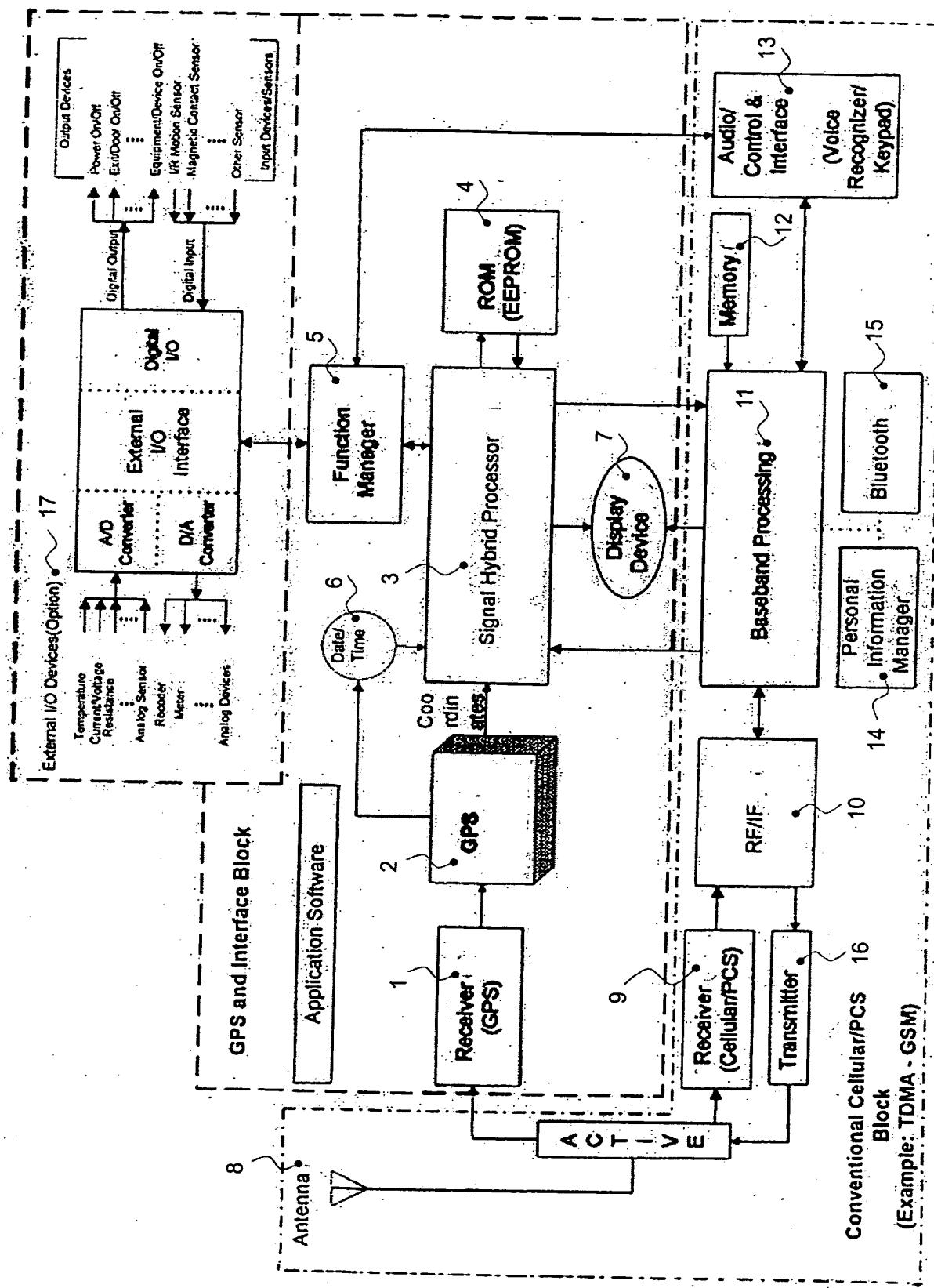
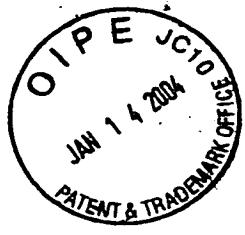


Fig. 3



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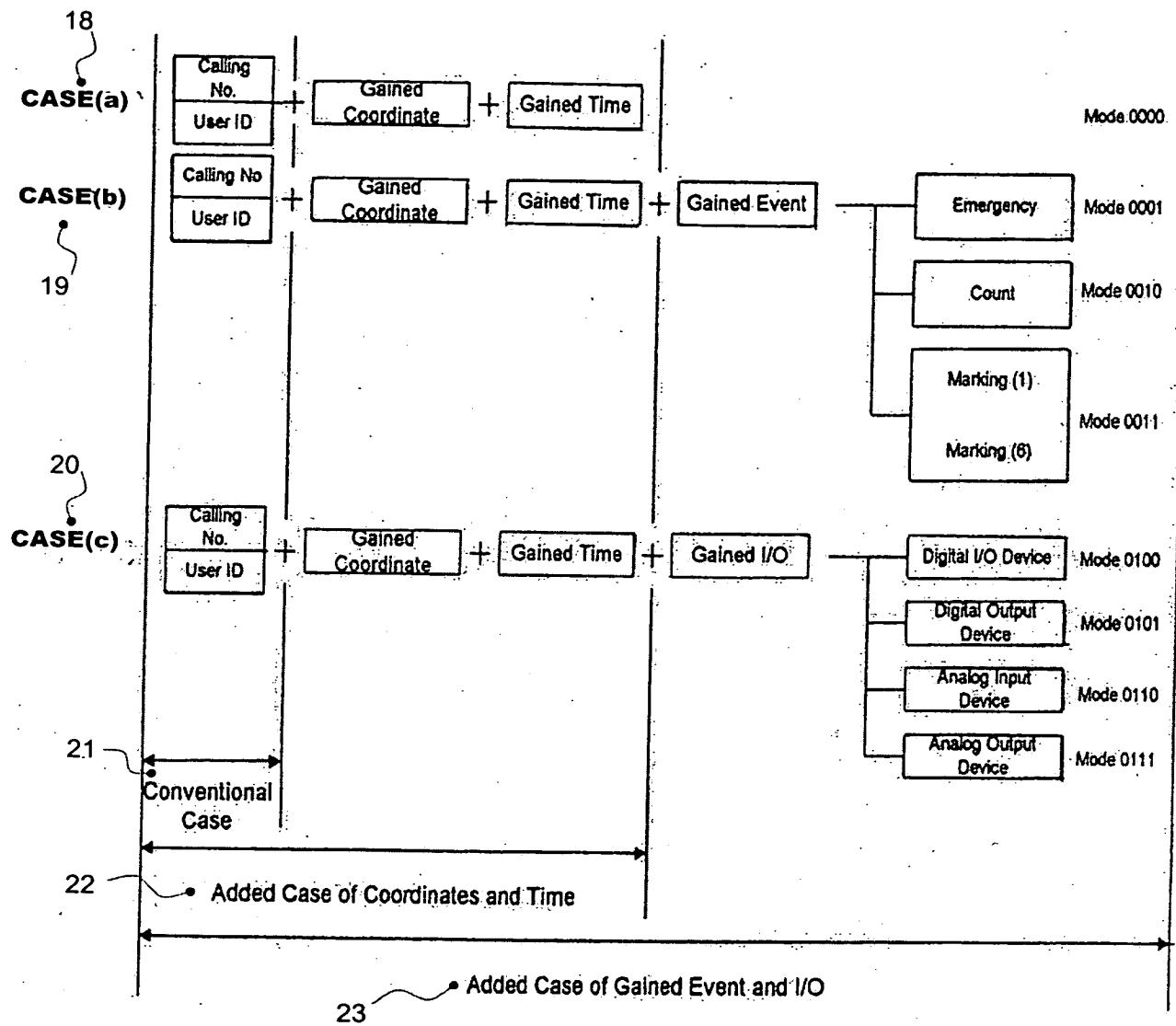
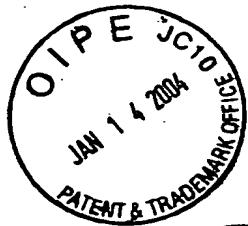


Fig. 4



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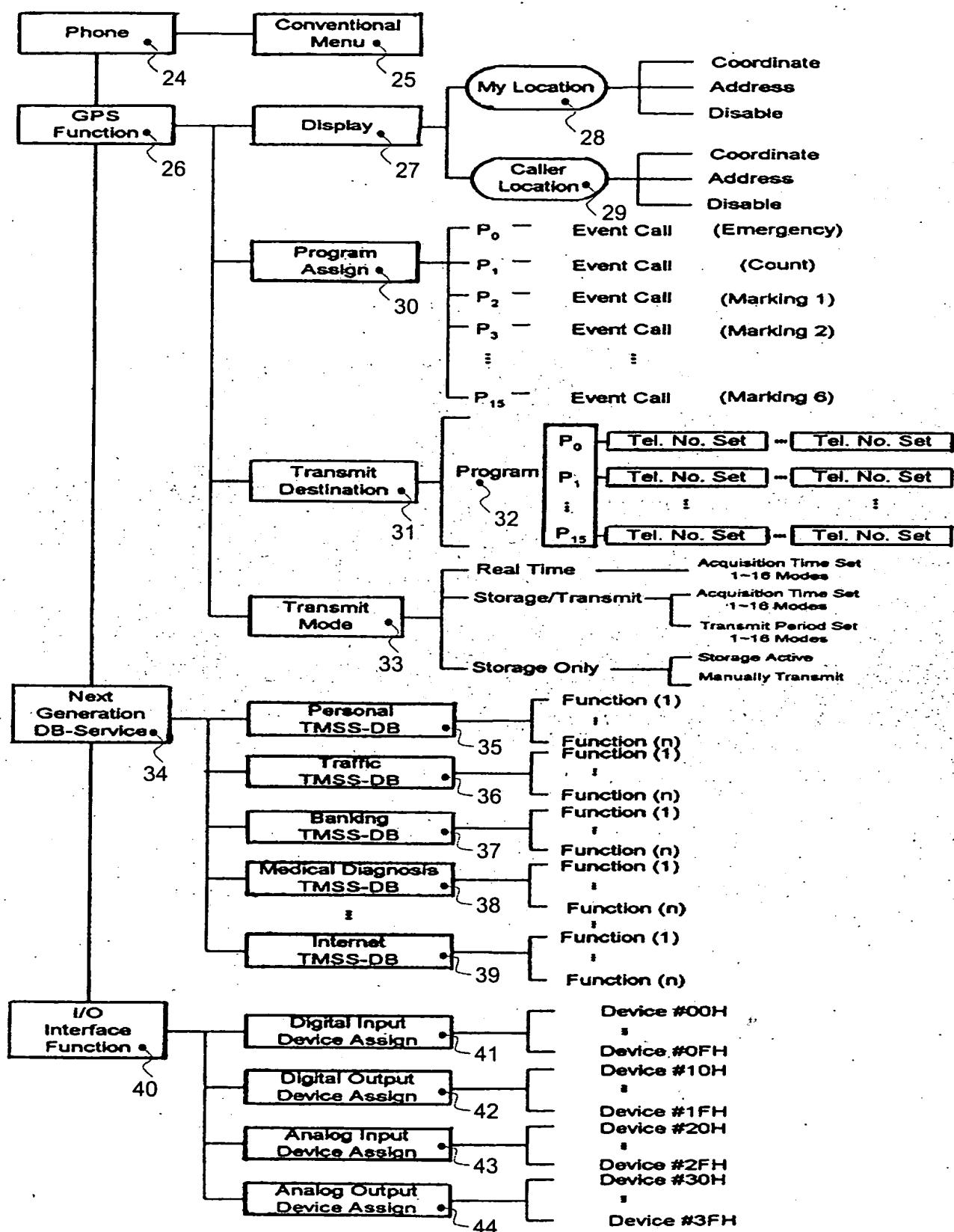
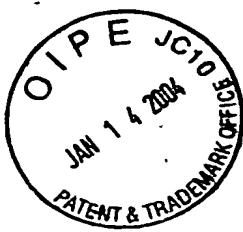
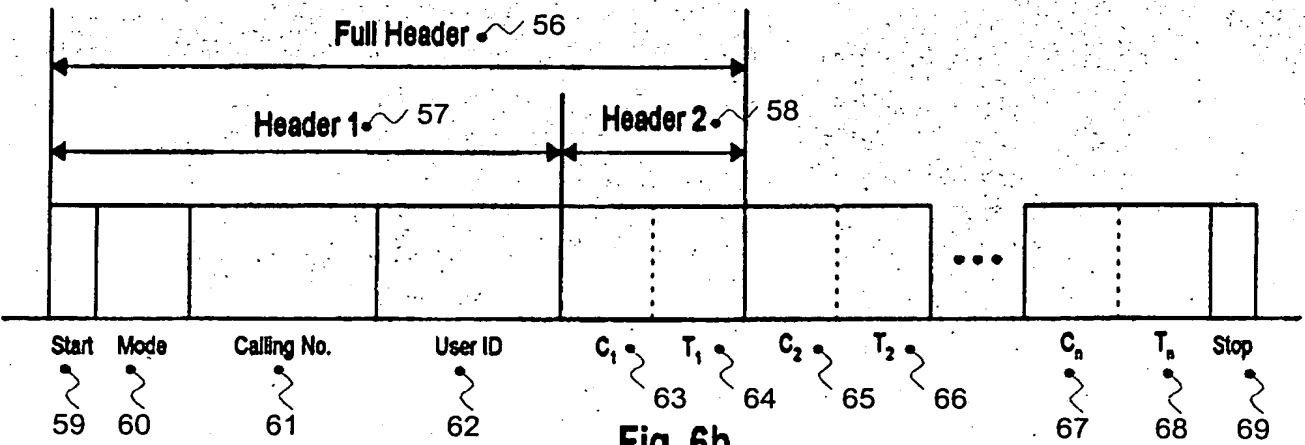
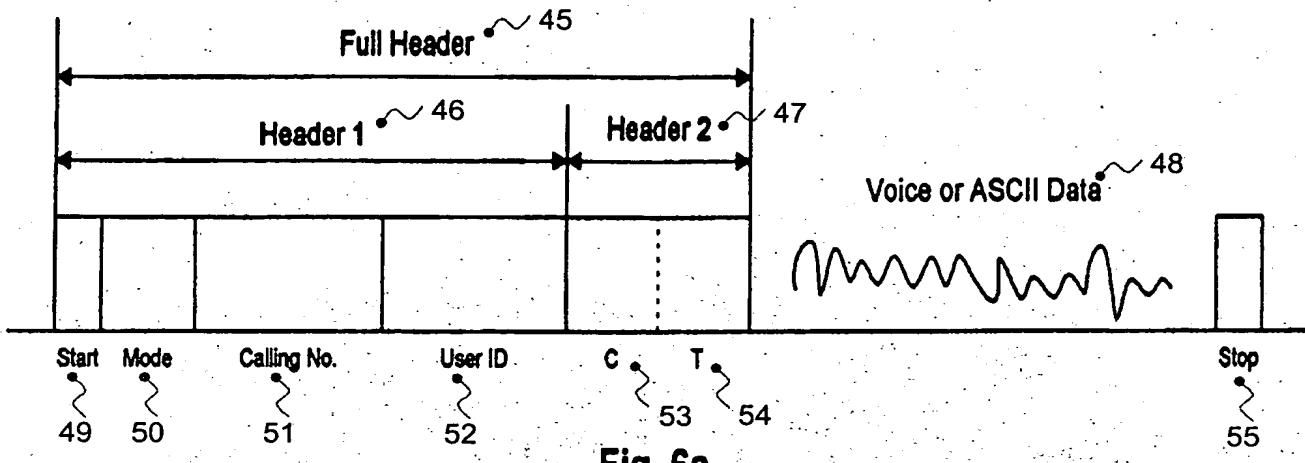


Fig. 5



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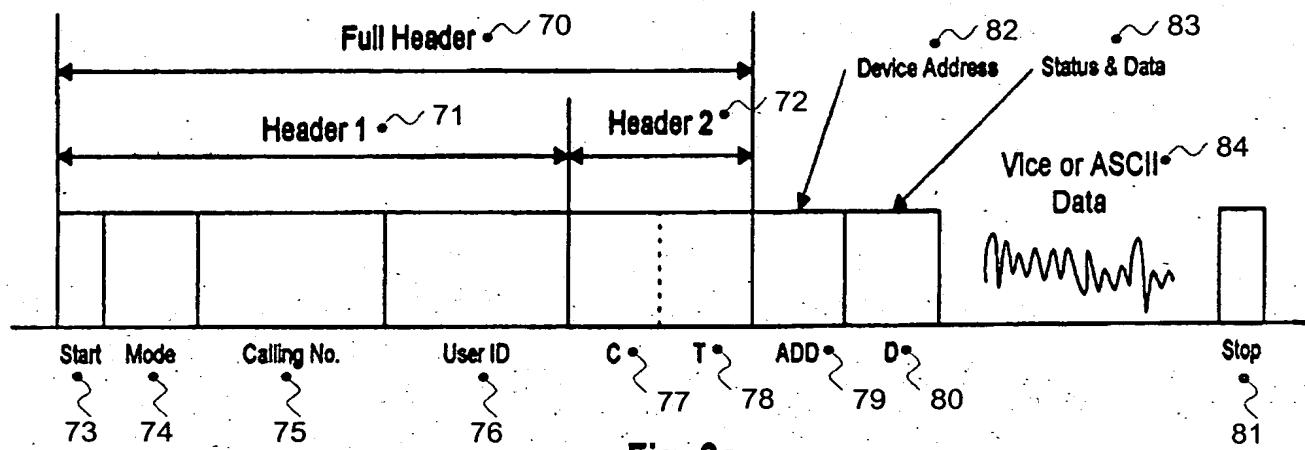


Fig. 6c

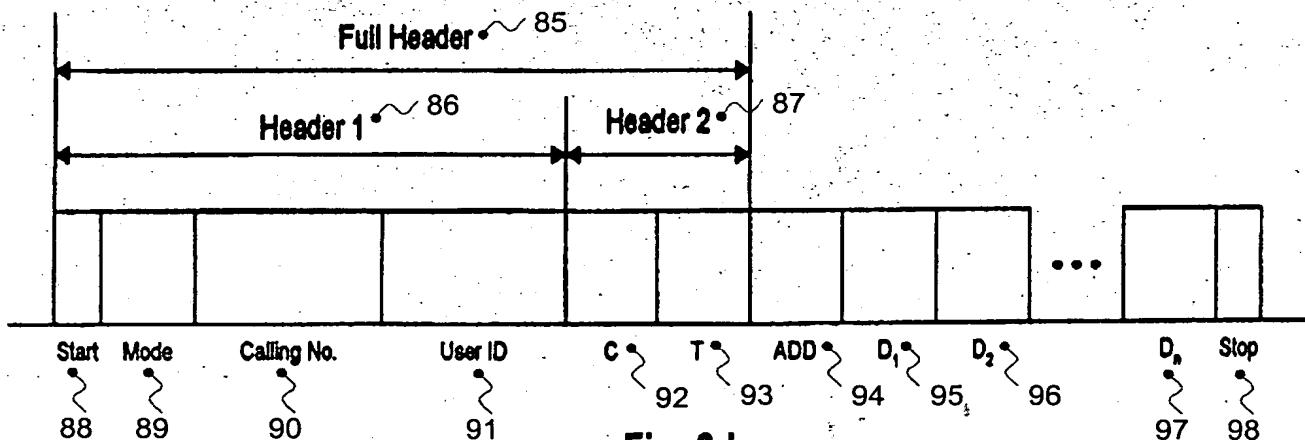


Fig. 6d



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Memory Address	GPS Data	Marking Time	Device Address	Status & Data	Mode
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99	0	0	0	0	0
100	0	0	0	0	0
101	0	0	0	0	0
102	0	0	0	0	0
103	0	0	0	0	0
104	0	0	0	0	0

Fig. 7

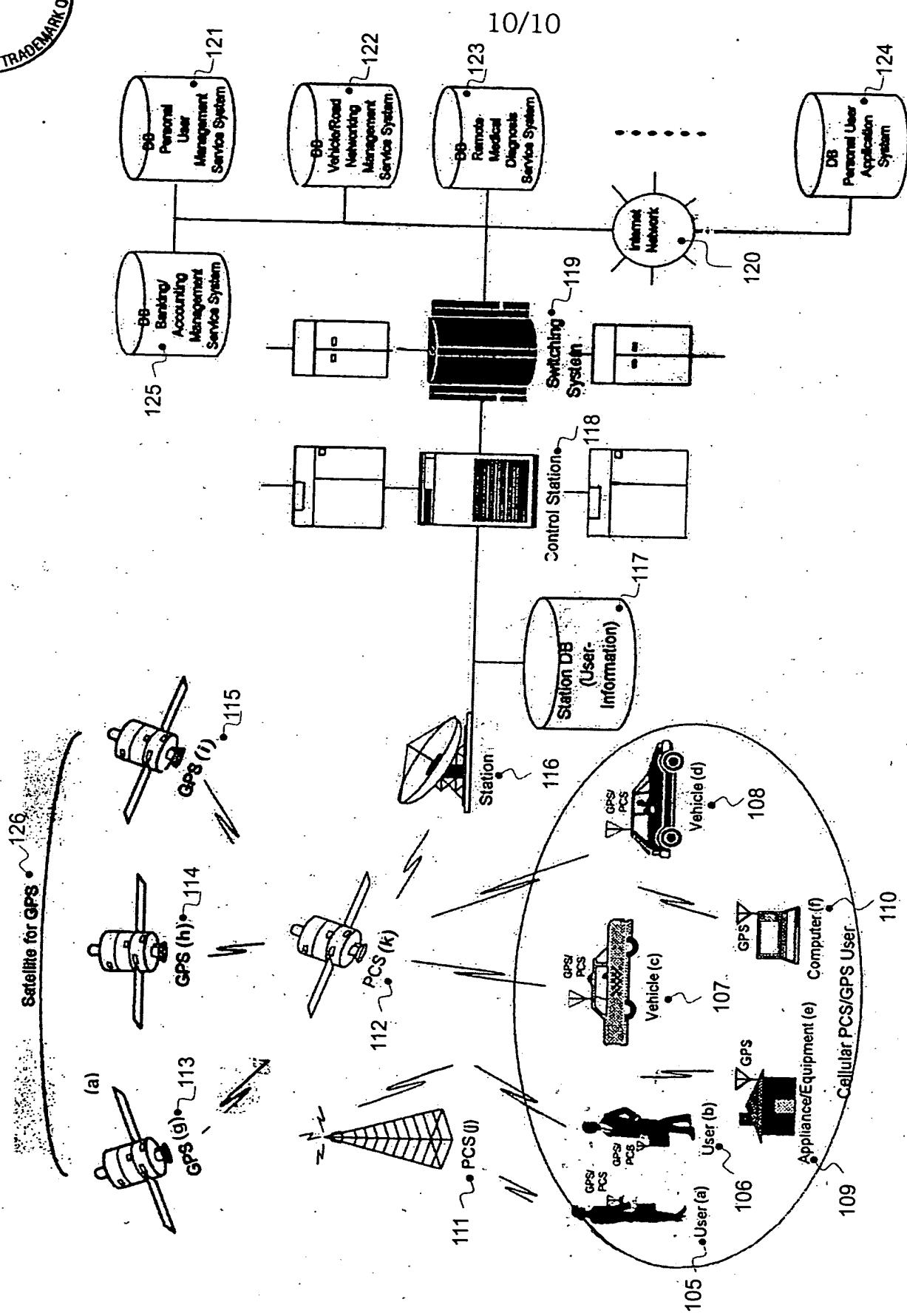
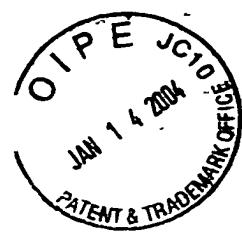


Fig. 8